



Department of Electronics Cochin University of Science and Technology, Cochin-682022, Kerala

Applications are invited from suitable candidates for summer internship programme under the project titled “DSP and AI workload tuned Embedded RISC-V Processors” under Chip to Startup (C2S) Program of Ministry of Electronics & Information Technology at CUSAT.

Sl. No	Position	Eligibility
1	Intern	(i) 3rd and 4th year students of BTech in Electronics/Electronics and Communication/Computer Science/ Instrumentation/ Electrical Engineering. OR (ii) 2nd year postgraduate students in the related specialization.

Nature & Duration of Engagement: 2 weeks and can be extended to a six-week activity or converted to an opportunity for the final year project for interested M.Tech. and B.Tech. students. The internship is an initiative to introduce undergraduate and postgraduate students to the area of VLSI Design and combines hands-on training with various hardware and software tools.

How to apply and selection process: The application is to be submitted online by attaching the necessary details in the following google form - [application form](#) on or before 04/05/2025. The selection will be made based on the academic record, an aptitude for research, accomplishments and area of research interest.

Shortlisted candidates will be informed by E-mail.

For any further clarification, contact: Department of Electronics, CUSAT

E-mail: c2s.cusat@gmail.com