

**DEPARTMENT OF ELECTRONICS, COCHIN UNIVERSITY OF SCIENCE AND TECHNOLOGY**  
**ACADEMIC CALENDAR 2025 – 2025 (ODD SEMESTER)**

<b>Date</b>	<b>1st MSc</b>	<b>1st MTech MWCE &amp; VLSI</b>	<b>3rd MSc</b>	<b>3rd MTech MWCE &amp; VLSI</b>
01/07/2025			Commencement of Odd Semester Classes	
09/07/2025	Commencement of Odd Semester Classes			
21/07/2025			Seminar Abstract Submission	Project Abstract Submission
14/08/2025	Publication of Attendance			
18/08/2025 to 22/08/2025	First Internal Examinations			First Midterm Project Evaluation
29/08/2025	Publication of First Internal Marks			
03/10/2025	Publication of Attendance			
06/10/2025 to 10/10/2025	Second Internal Examinations			Second Midterm Project Evaluation
17/10/2025	Publication of Second Internal Marks			
21/10/2025 to 24/10/2025			Seminar Presentation	
24/10/2025	Publication of Consolidated Internal Marks			
31/10/2025	End of Odd semester Classes			
01/11/2025	Publication of Consolidated Attendance			
12/11/2025	Commencement of Regular/Supplementary Examinations			Project Report Submission
17/11/2025 to 19/11/2025				Final Project Evaluation
21/11/2025	Completion of Regular/Supplementary Examinations			

**Head of the Department**