Notice

Principal Investigator, project titled "DSP and AI workload tuned Embedded RISC-V Processors" under Chip to Startup (C2S) Program of Ministry of Electronics & Information Technology" invites applications from candidates for the recruitment in various research posts on a temporary basis as per the following eligibility conditions.

POSITIONS IN DEPARTMENT OF ELECTRONICS

Sl.No.	Position	Eligibility
1.	Research Associate – I Electronics	Ph.D or having 3 years of research, teaching and design and development experience after ME/MTech with at least one research paper in SCI journal.
2.	Junior Research Fellow Electronics	MTech/ME/MS(R)/M.Sc. degree in VLSI Design/VLSI and Embedded System/Microelectronics and VLSI Design/Electronics/Electronics and Communication /Digital Systems/Computer Science and other relevant areas of Electronics Engineering with a) valid CSIR-NET/GATE b)have cleared equivalent national level eligibility tests conducted by agencies or institutes such as DST, DBT, DAE, DOS, DRDO, MoE, ICAR, IMR, IIT, IISc, IISER, NISER etc.
3.	Project Associate – I Electronics	BTech Degree in Electronics/Electronics and Communication / Computer Science and other relevant areas of Electronics Engineering

POSITIONS IN DEPARTMENT OF COMPUTER SCIENCE

Sl.No.	Position	Eligibility
4.	Research Associate – I Computer Science	Ph.D or having 3 years of research, teaching and design and development experience after ME/MTech with at least one research paper in SCI journal.
5.	Junior Research Fellow Computer Science	MTech/ME/MS(R)/M.Sc. degree in Computer Science a) valid CSIR-NET/GATE b)have cleared equivalent national level eligibility tests conducted by agencies or institutes such as DST, DBT, DAE, DOS, DRDO, MoE, ICAR, IMR, IIT, IISc, IISER, NISER etc.

6.	Project Associate – I Computer Science	BTech/BE Degree in Computer Science or in other relevant areas of Computer Science and Engineering

General information:

1.	Desirable Qualifications for the above posts in Department of Electronics, CUSAT	Exposure to processor ISA and design , Experience in design and verification, Experience in EDA Tools, Understanding of Linux Environment and open source tools, Experience to handle VLSI training
2.	Desirable Qualifications for the above posts in Department of Computer Science, CUSAT	Exposure to LLVM/GCC compilers, code generation, software programming in C++, knowledge about compiler optimizations, Processor ISA and SIMD instructions.
3.	Nature & Duration of Engagement	1 year which is extendable up to three years based on review. Selected candidates will be encouraged to register for Ph.D Programme as a full time research scholar at CUSAT on qualifying the DAT Examination of the concerned Department.
4.	Fellowship	As per rules approved by DST. Revision of emoluments and guidelines on service conditions for research personnel engaged in R&D programme of the Central Government Departments/Agencies[PDF] OM - Scientific_Technical Manpower-Revised Guidelines.pdf
5.	Mode of Application	A detailed resume highlighting experience in the relevant area of the project may be sent to **c2s.cusat@gmail.com** on or before 15/09/2023. Subject of the email should clearly state the name of the position applied for.
6.	Mode of Recruitment	Interview - Online/Offline Mode
7.	Time, date and venue of the interview	The shortlisted candidates will be notified of the interview at a later date.

Kochi – 22   Principal Investigator,

1-09-2023   C2S Program CUSAT

                                                         Dr. Tripti S Warrier