

Patrons

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Who can Attend?

UG/PG Students, Research Scholars and Faculty from ECE, EEE and CSE or from any stream who are passionate about VLSI domain.

Registration Details

REGISTRATION LINK: <https://forms.gle/QLjZ4rg1TaWFFAcc7>

Registration deadline: **01-December-2023**

Shortlisting is on First Come First Serve Basis

Certificates will be given ONLY to participants who attend the program in-person.



Cochin University of Science and Technology

Re-accredited by NAAC with 'A+' Grade

കൊച്ചി ശാസ്ത്ര സാങ്കേതിക സർവ്വകലാശാല



इलेक्ट्रॉनिक्स एवं
सूचना प्रौद्योगिकी मंत्रालय
MINISTRY OF
**ELECTRONICS AND
INFORMATION TECHNOLOGY**
सत्यमेव जयते

Hands-on workshop & FDP on VLSI EDA Tools for FPGA & ASIC Design

Organized by
Department of Electronics
Cochin University of Science and Technology

4th – 8th December, 2023



IEEE Kochi Subsection

About CUSAT

Cochin University of Science and Technology (CUSAT) was initially constituted as the University of Cochin through an Act of Kerala Government on 10th July 1971. The University of Cochin was re-constituted as Cochin University of Science and Technology in February 1986, redefining its objectives as “promoting Graduate and Post-Graduate studies and



Advanced Research in Applied Sciences, Technology, Industry, Commerce, Management and Social Sciences.” CUSAT is now a world-ranking university with the specific purpose of nurturing higher education, emphasizing post-graduate studies and research in applied science, technology, industry, humanities, and commerce. CUSAT has also found a place in the QS World University Ranking and the National Institutional Ranking Framework (NIRF) of the Indian Government. The research activities are supported and funded by the Central Government agencies like UGC, DST, DBT, MHRD, etc., as well as by various international research partners through various MoUs.

About Department of Electronics

Established in 1975 by the University of Cochin, the Department of Electronics offers dynamic post-graduate programs, including Master’s and Doctoral degrees, shaping future leaders in electronics. With a focused



curriculum emphasizing both depth and breadth, our Master’s program places motivated minds in smaller classrooms with expert professors. As part of the Faculty of Technology, we provide courses in traditional and specialized areas, including Machine Learning, Deep Learning, FPGA-Based Design, EMI/EMC. Through commitment and hard work, aspirants develop competencies, advance careers, and make a significant impact on the world. Our program, which includes Ph.D. opportunities, guides them to unlock leadership potential, fostering meaningful learning and addressing systemic challenges in educational landscapes.

Workshop Overview

This program is crafted to offer participants a comprehensive grasp of embedded systems and Application-Specific Integrated Circuit (ASIC) design, encompassing fundamental concepts and methodologies. The workshop is geared towards exploring the Embedded Design Flow, emphasizing a hands-on approach with widely used FPGA platforms such as Zedboard and Zybo boards. Attendees will acquire practical insights into the entire development process, spanning from conceptualization to implementation.

Additionally, participants will be familiarized with industry-standard tools for Digital Design Flow, gaining practical experience in digital design using Cadence EDA tools. The program features sessions led by accomplished speakers and is planned to be conducted in an offline format.

Workshop Contents

- ▶ Embedded design Flow using Zedboard and Zybo boards
- ▶ Introduction to ASIC Design Flow
- ▶ Verification Methodologies
- ▶ Application tailored processor design - a case study
- ▶ Digital Design Flow using Cadence EDA Tools

Resource Persons

- ▶ Prof.(Dr.) Alex P James, Digital University Kerala
- ▶ Experts from CoreEL Technologies
- ▶ Experts from Entuple Technologies

