

## Program Schedule

**One Week Technical Workshop on Frontiers in RF Communications and Circuit Design at the Dept of Electronics, CUSAT**  
**27<sup>th</sup> Feb - 4<sup>th</sup> March 2020`**

	9.00am	09.30am	10.00am	10.30am	12.00pm	1.00pm	2.30pm	2.45pm
27-Feb Day 1	Registration begins	<b>Inauguration</b>	Tea	<b>IEEE APS DL</b> <i>Analysis of Phased Array Antennas</i> Dr. Arun K Bhattacharyya, USA	lunch	<b>Digital Radio Broadcasting</b> Mr. Rajith Nair Inntot Technologies Pvt Ltd.	Tea	<b>Antenna Array Synthesis</b> Dr. Saumya Adhikari, PES University, Bangalore
28-Feb Day 2	9.30am		11.00am	11.15am	12.45pm	1.30pm		
	<b>IEEE APS DL</b> <i>Beam Synthesis in Phased Arrays</i> Dr. Arun K. Bhattacharyya, USA		Tea	<b>Trends and Technologies in Avionics and Electronic Warfare</b> Gp. Capt. Babu Joseph, Tech. Consultant, Bangalore	lunch	<b>Hands-on Session</b> <i>Agilent ADS</i>		
29-Feb Day 3	<b>DoE Alumni Lecture</b> <i>Automotive EMC</i> Mr. Vineesh S., Valeo Chennai		Tea	<b>Hands-on Session</b> <i>Agilent ADS</i>	lunch	<b>Hands-on Session</b> <i>Agilent ADS</i>		
01-Mar Day 4	SUNDAY							
02-Mar Day 5	<b>Hands-on Session</b> <i>Next Generation of Communication System Design using NI SDR USRP Platform &amp; RF Circuit Design using NI AWR Software</i>							
03-Mar Day 6	<b>5G System Design -1</b> Mr. Praveen Kumar, Intel, Bangalore		Tea	<b>5G System Design -2</b> Mr. Praveen Kumar, Intel, Bangalore	lunch	<b>DoE Alumni Lecture</b> <i>Simulation of Electrically Large Problems</i> Mr. Sumith S Pillai Designtech Systems Ltd, Bangalore		
04-Mar Day 7	<b>DoE Alumni Lecture</b> <i>Overview and Future Trends</i> Prof. K. J. Vinoy, IISc, Bangalore		Tea	<b>Innovation Engineering</b> Dr. T. J. Apren, SFO Tech, Trivandrum	lunch	<b>Valedictory</b>		