

## Schedule for 5 day Online Workshop on The Art of IC Design Using EDA

| Date                    | Time                | Session   | Speaker  |
|-------------------------|---------------------|---|--|
| 15-03-2021<br>Monday    | 9.30 AM - 10.00 AM  | Inauguration  |  |
|                         | 10.15 AM - 11.45 AM | Expert Session on Custom IC Design  | Dr. K R Viveka,<br>Post Doctoral Fellow<br>National Univeristy of<br>Singapore   |
|                         | 11.45 AM - 1.00 PM  | Introduction to Basic Linux<br>Commands and Semi-Custom IC<br>Design Flow                       | Entuple Technologies   |
|                         | 2.00 PM - 3.15 PM   | ASIC/SoC Design Flow Overview -<br>Front End  |  |
|                         | 3.30 PM - 4.30 PM   | ASIC/SoC Design Flow Overview -<br>Back End   |  |
| 16-03-2021<br>Tuesday   | 9.30 AM - 11.30 AM  | Illustrative Lab: 4-bit Counter<br>(Synchronous and Asynchronous) -<br>Simulation and Synthesis | Entuple Technologies   |
|                         | 11.45 AM -1.00 PM   |   |  |
|                         | 2.00 PM - 4.30 PM   | Hands-On session  |  |
| 17-03-2021<br>Wednesday | 9.30 AM -10.45 AM   | Expert Session on Design<br>Verification  | Mr. Priyadarshan,<br>Ignitarium Technogy<br>Solutions Ltd.                       |
|                         | 11.00 AM -1.00 PM   | Introduction to Full Custom Design<br>Flow  | Entuple Technologies   |
|                         | 2.00 PM - 3.30 PM   | Expert Session on FPGA Based<br>System Design   | Mr. Abi K Krishnan,<br>National Physical<br>Oceanographic<br>Laboratories, Kochi |
|                         | 3.30 PM - 4.30 PM   | Hands-On session  |  |
| 18-03-2021<br>Thursday  | 9.30 AM to 10.45 AM | Expert Session on Digital IC Design   | Dr. Subramanyam Mula,<br>Assistant Professor,<br>IIT Palakkad                    |
|                         | 11.00 AM -1.00 PM   | Illustrative Lab: Design of CMOS<br>Inverter  | Entuple Technologies   |
|                         | 2.00 PM - 4.30 PM   | Hands-On session  |  |
| 19-03-2021<br>Friday    | 9.30 AM -10.45 AM   | Expert Session on Analog Design   | Mr. Nitin Thomas,<br>PhD Scholar, NIT Calicut                                    |
|                         | 9.30 AM - 1.00 PM   | Illustrative Lab: Design of Common<br>Source Amplifier  | Entuple Technologies   |
|                         | 2.00 PM - 4.30 PM   | Hands-On session  |  |
|                         | 4.00 PM - 4.30 PM   | Feedback and Valedictory Session  |  |